

Fig. 1

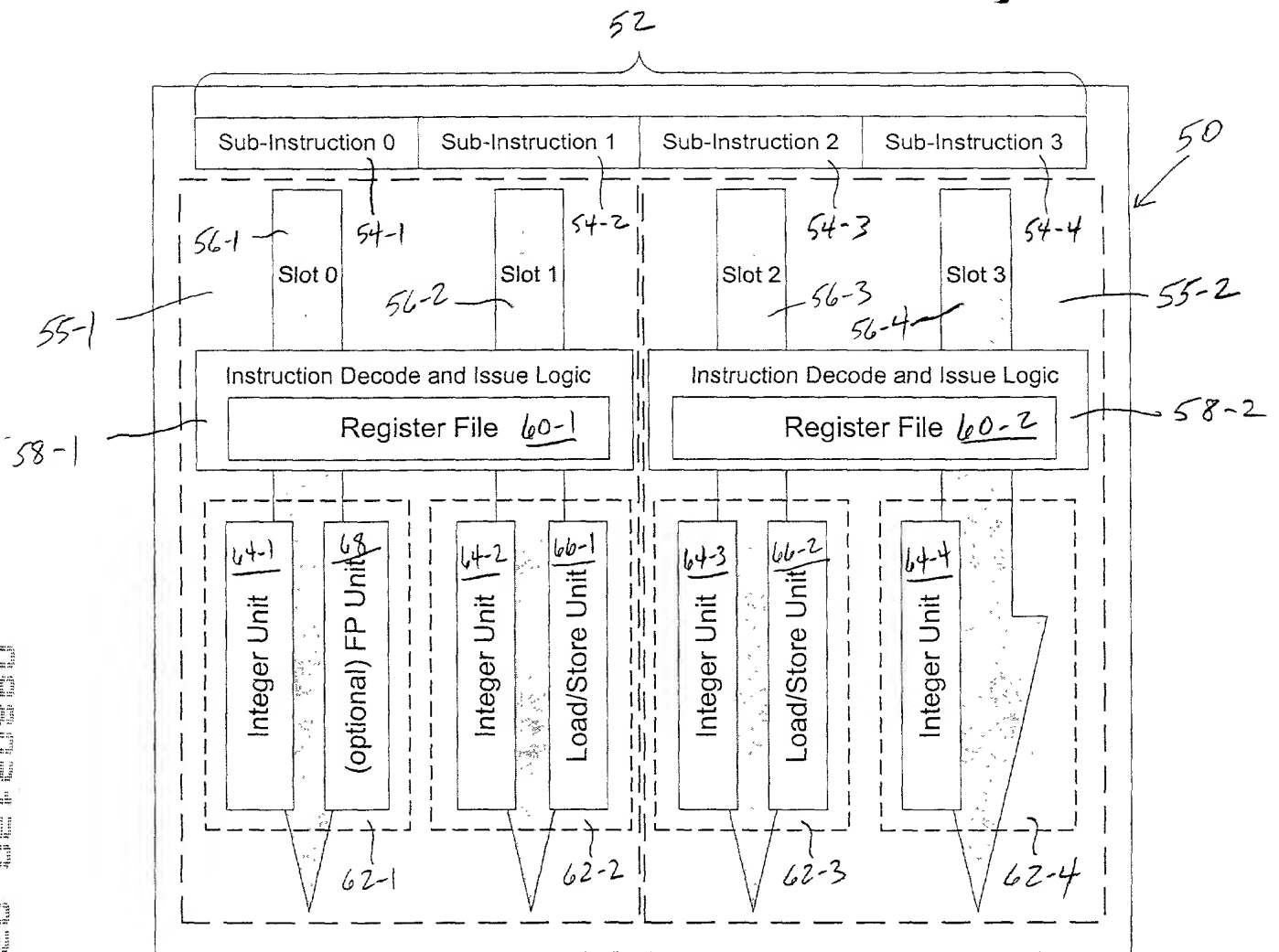


Fig. 2

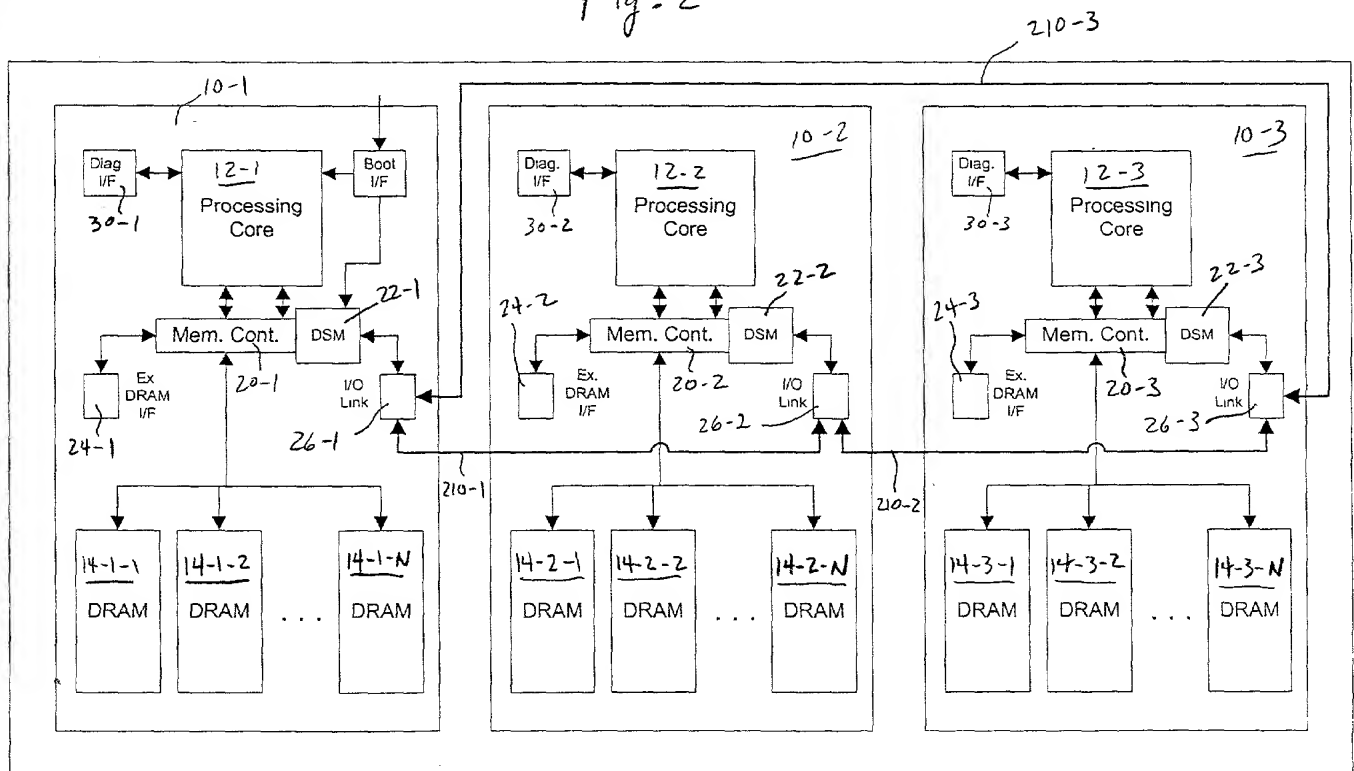


Fig. 5

100
Fetch
Stage

110
Decode
Stage

120
Execute
Stage

130
Writeback
Stage

140
Trap
Stage

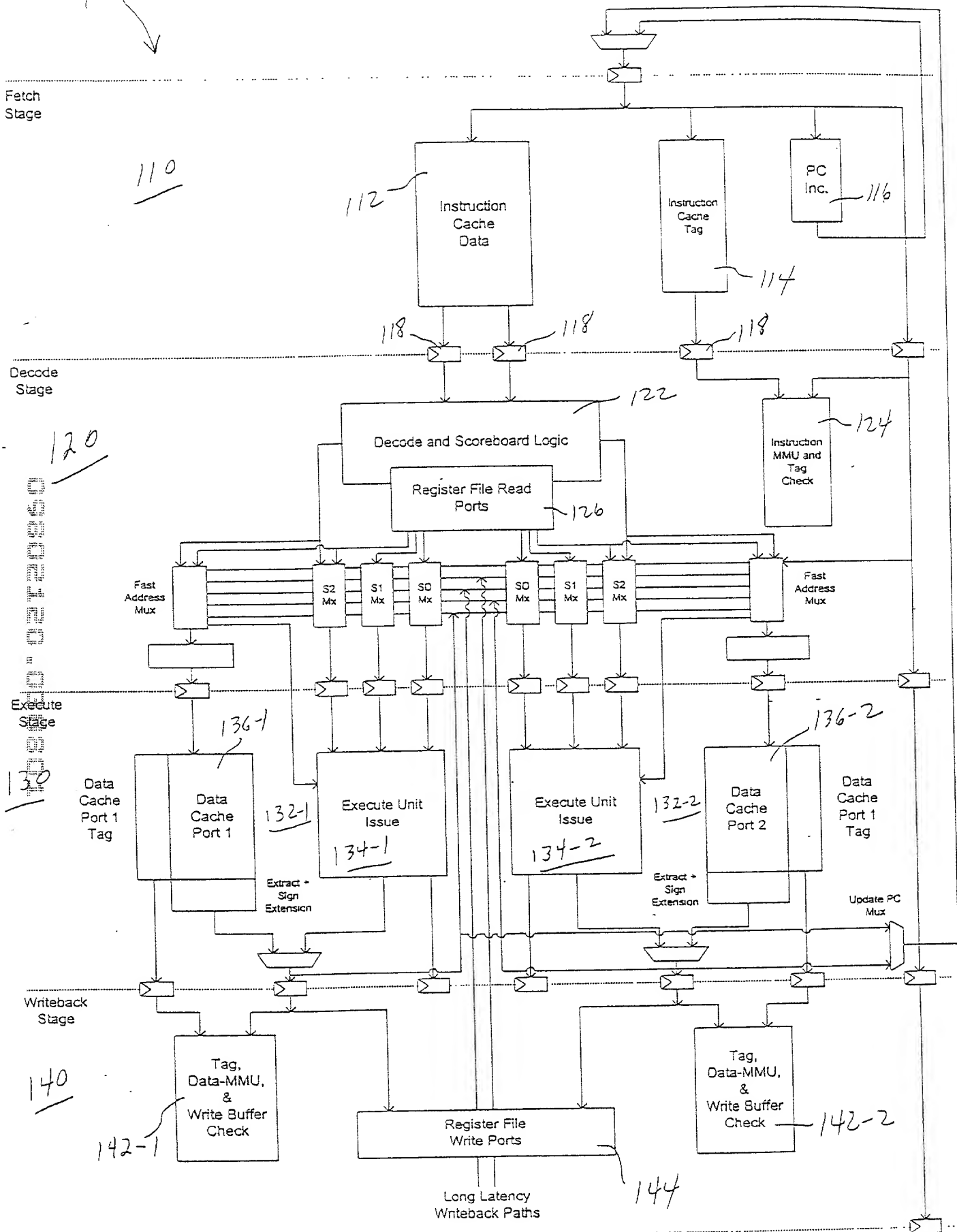


Fig. 3

80 81 82 83 84 85

863

R0

R1

R2

R3

R4

R62

R63

60

Fig. 4